

Title: Distributed micro instruction set processor architecture for high-efficiency signal processing

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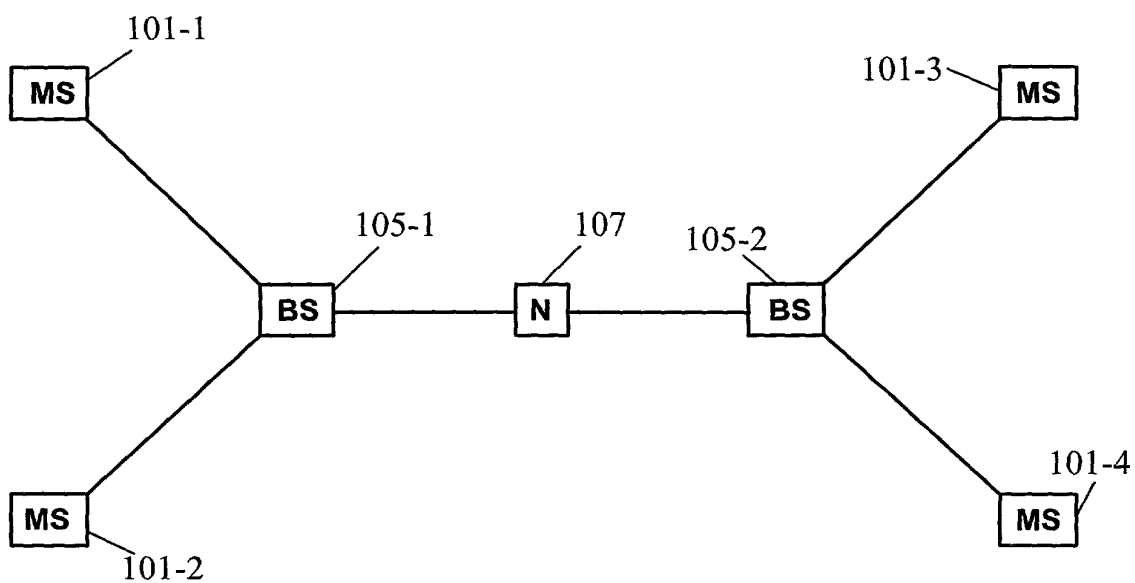


FIG. 1

VMI Objects

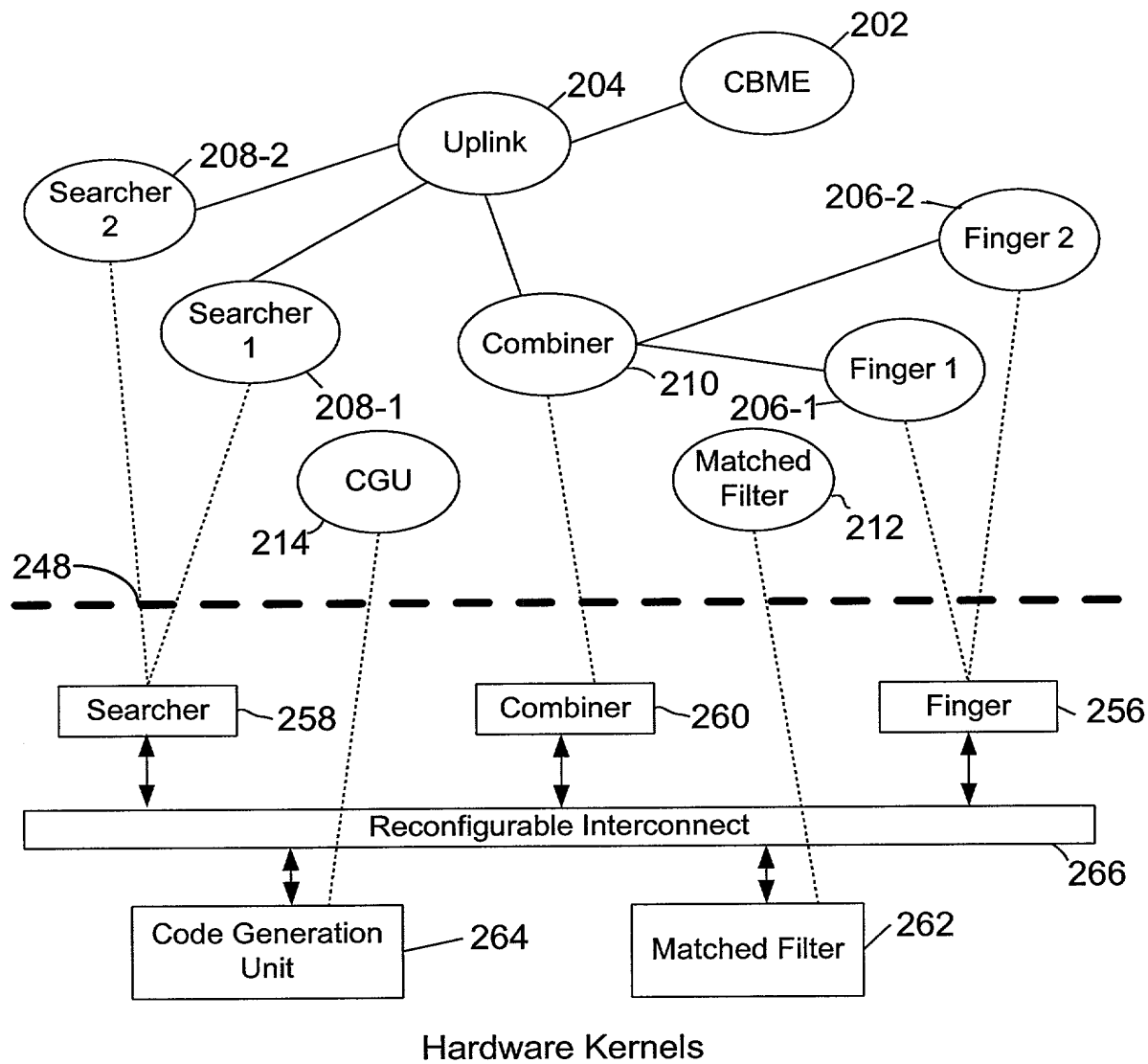


FIG. 2

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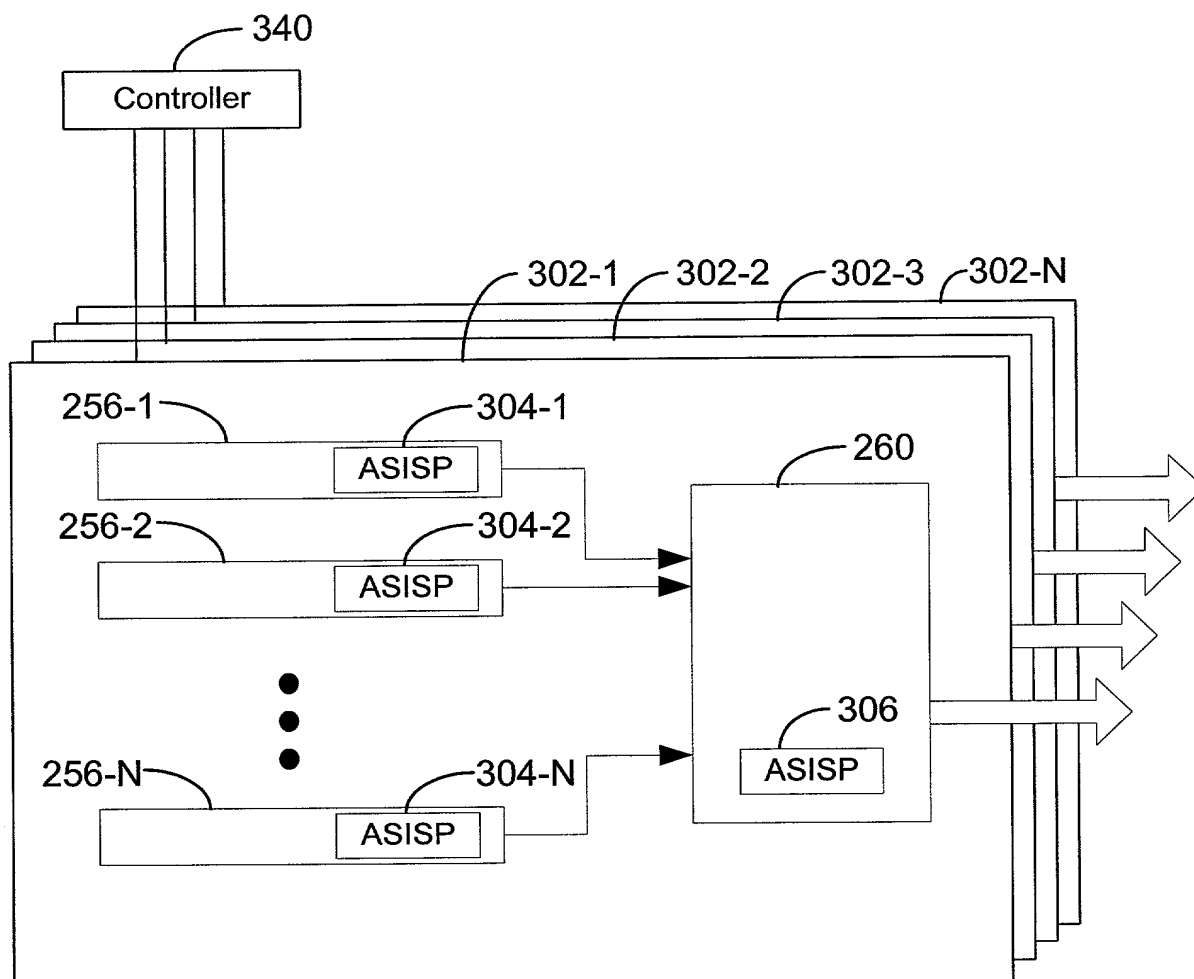


FIG. 3

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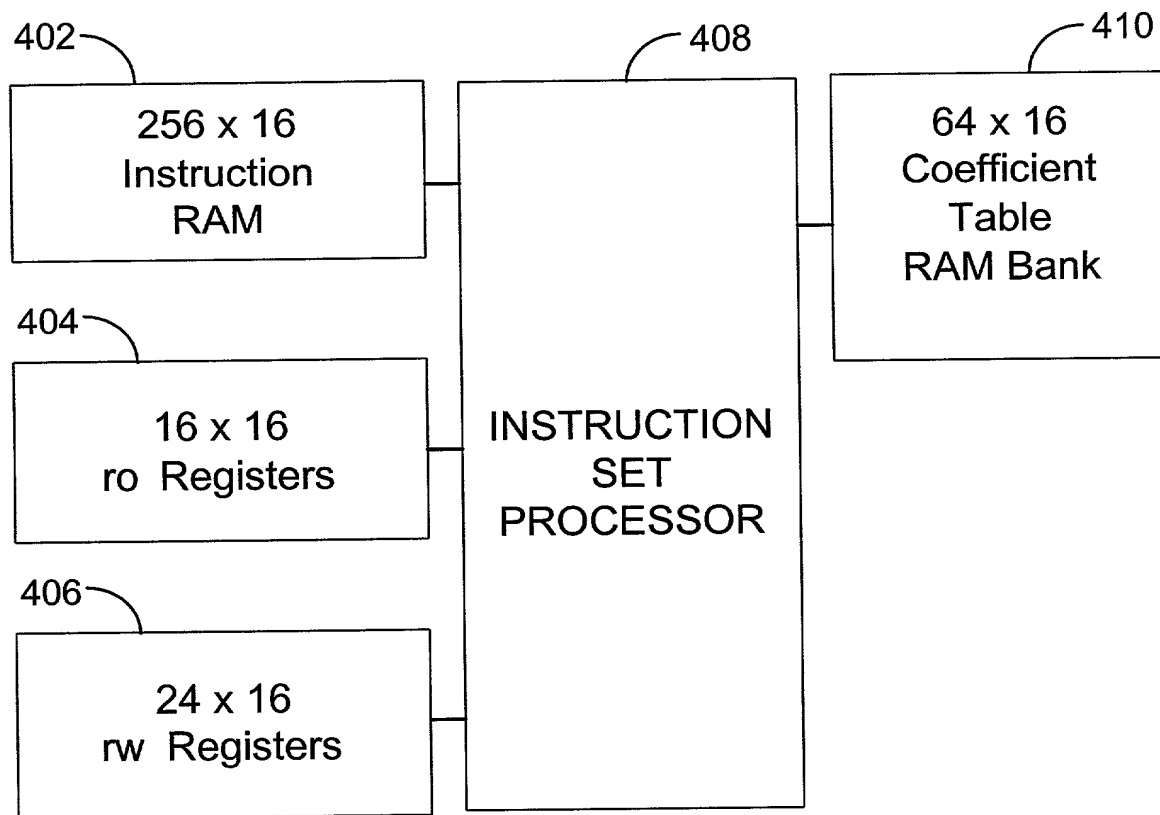


FIG. 4

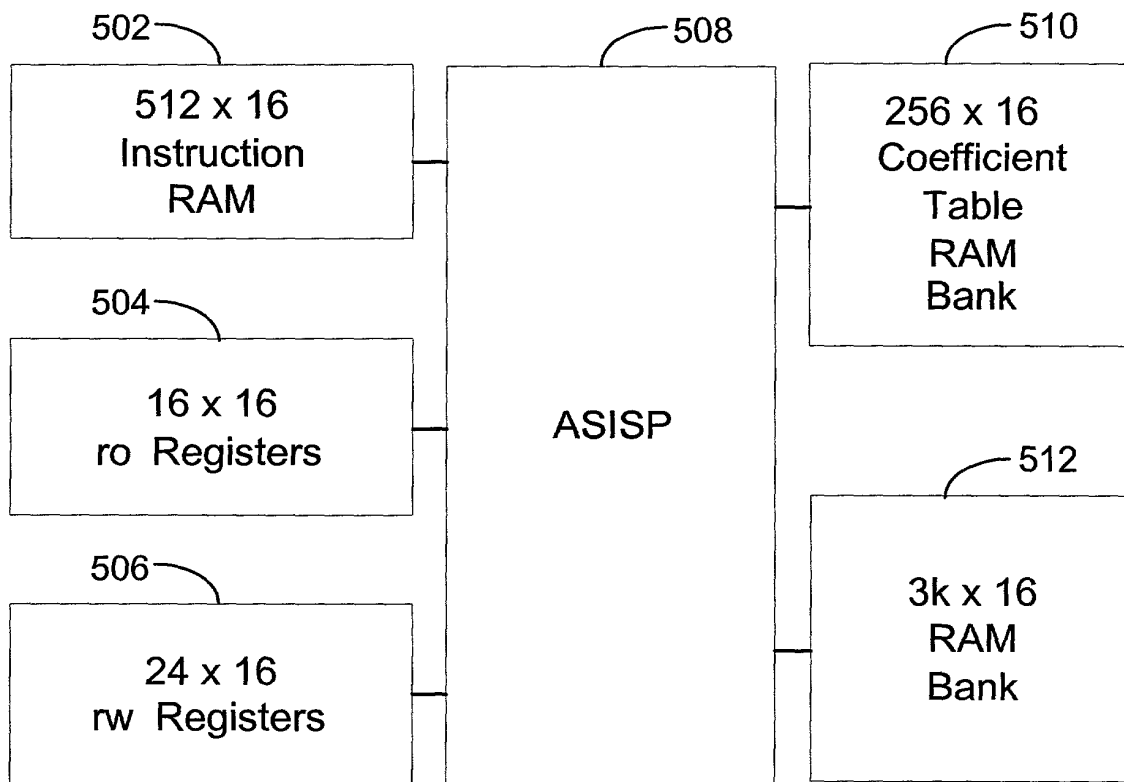


FIG. 5

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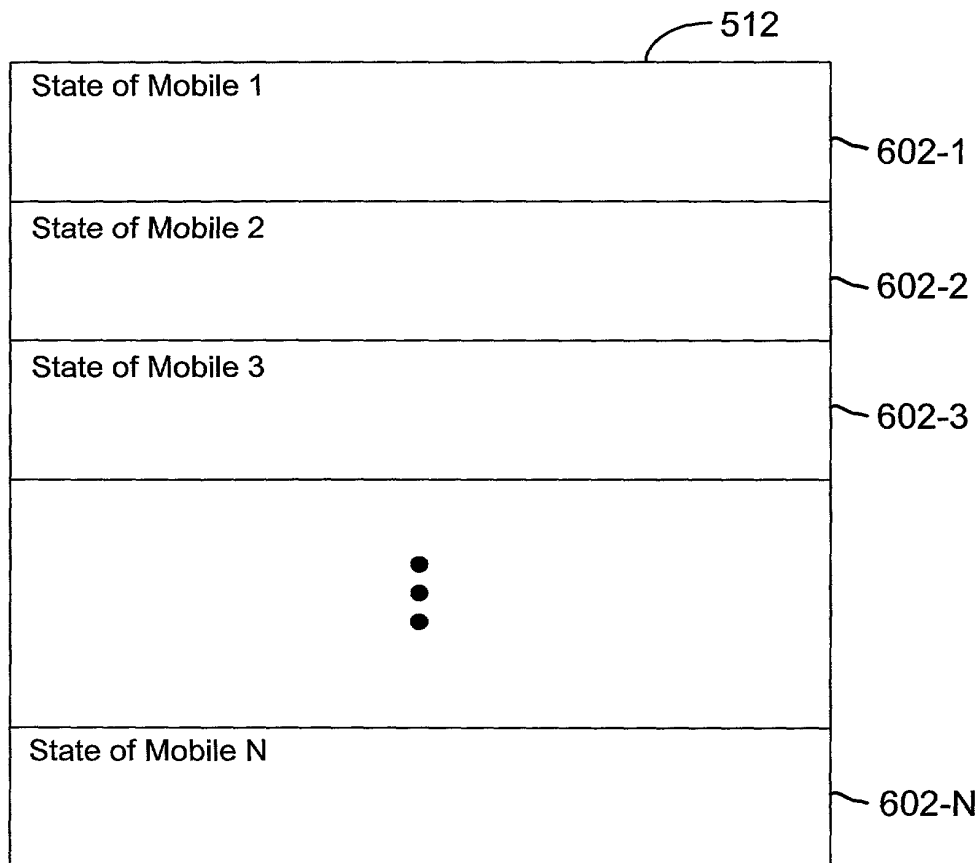


FIG. 6

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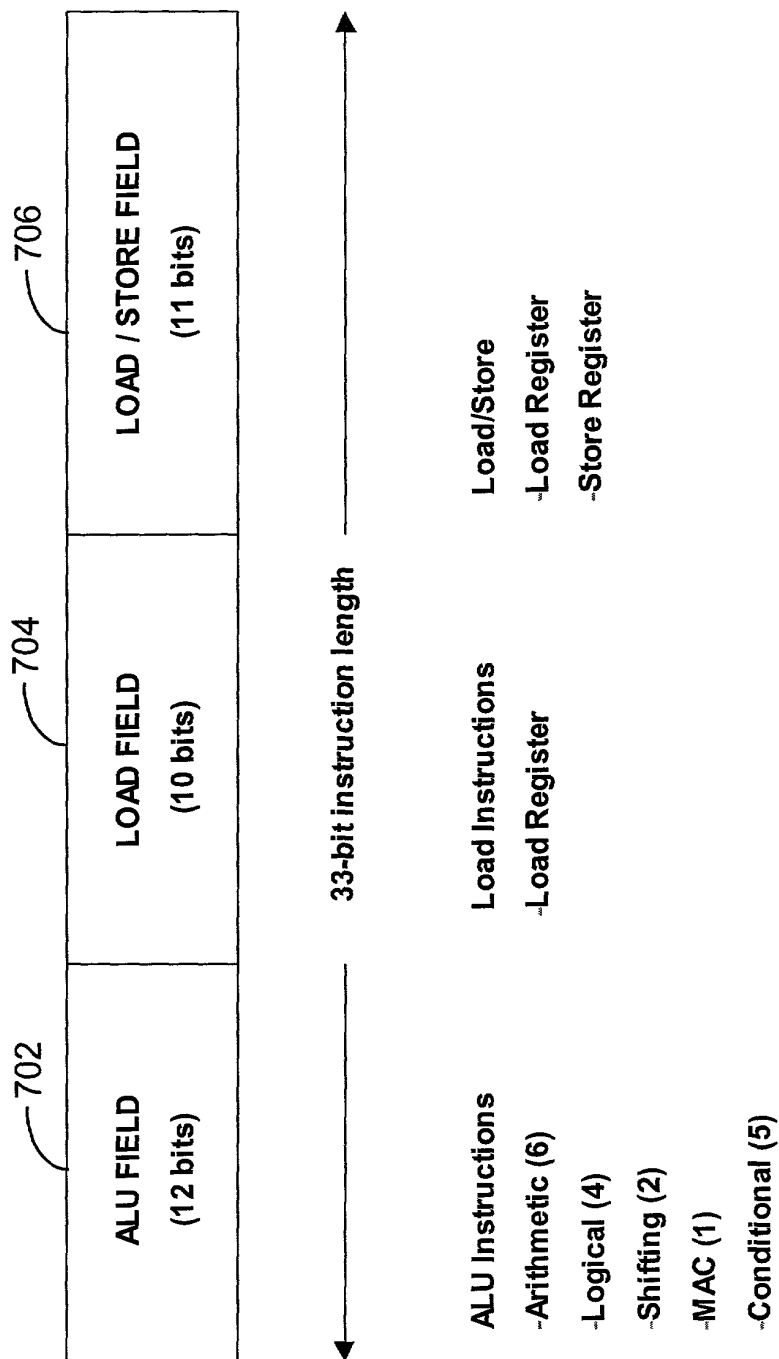


FIG. 7

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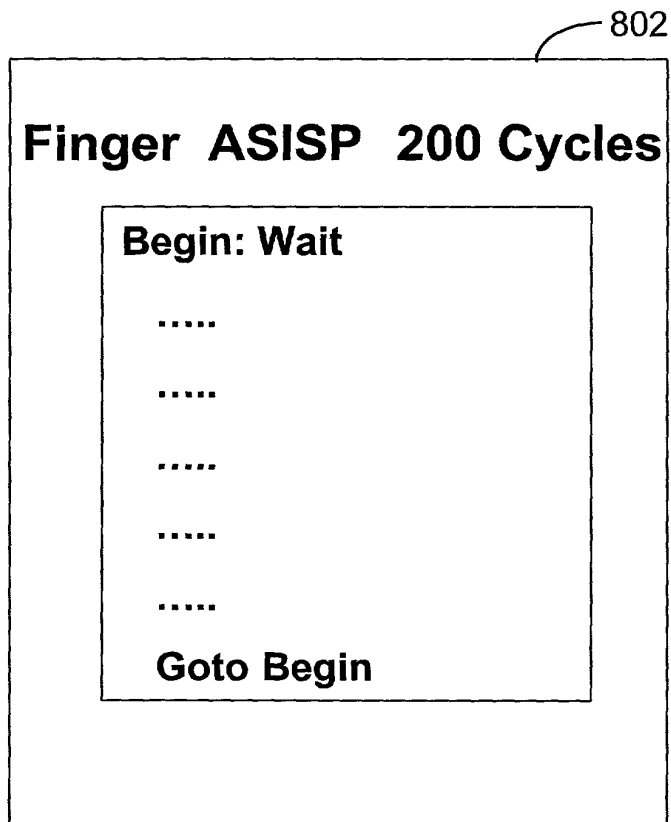


FIG. 8



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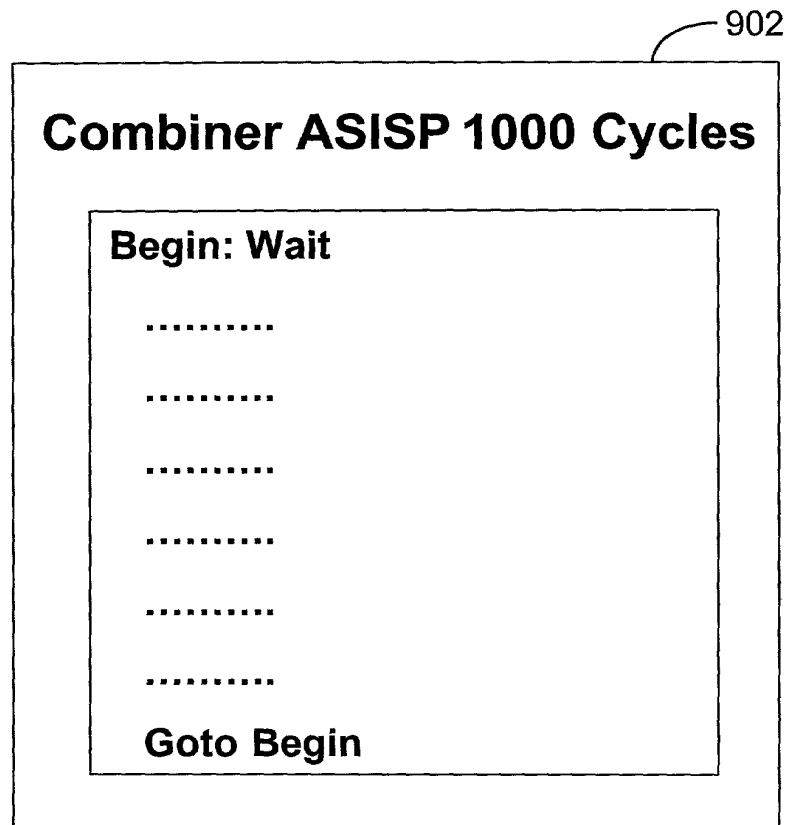


FIG. 9